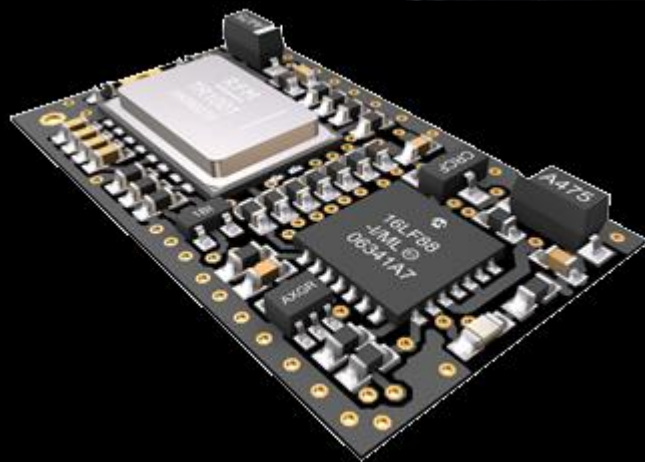


# Migrating from TR-7xD to TR-7xG

(TR-7xG a pohledu programátora)



# Back to Black



TR-21A [2007]



TR-72GA [2022]

# D vs. G Comparison



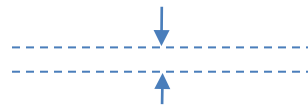
	TR-7xD	TR-7xG
Form factor	✓ Same	
Pinout	✓ Same	
MCU	PIC16LF1938	PIC16LF18877
Flash	16 KW	32 KW
RAM	1 KB	4 KB
EEPROM	256 B	256 B

- All GPIOs: Interrupt on Change , Pull-up, Open Drain, Slew Rate, ST or TTL, A/D Input
- PPS: “any” digital peripheral remappable to “any” GPIO
- Timers: +2 16bit timers
- New peripherals:  
CRC, Peripheral Module Disable (PMD), Complementary Waveform Generator (CWG), Zero-cross Detection (ZCD), Configurable Logic Cell (CLC), Numerically Controlled Oscillator (NCO), Data Signal Modulator (DSM), Signal Measurement Timer (SMT)

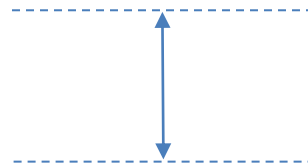
- .hex files are not binary compatible
- PIC registers at different addresses (underscore symbols)
- Unused peripherals must be enabled by PMD  
(and then re-configured)
- Peripherals “must” be mapped via PPS

Custom DPA Handler flash memory increased

from 864 to



**5344**



instructions 😊

AC/DC 

Enjoy Black in Black  
Development!